

## **REMARKS**

Claims 1-5 remain pending in this application. No new matter is being added. Support for the amendment to the claims can be found in the specification and drawings, for example, at least in paragraphs [0017], [0020], and [0023]. Reconsideration of this application is respectfully requested.

### **Claim Rejections - 35 USC § 112**

Claims 12, 15-18 and 22-23 were rejected under 35 USC 112, second paragraph as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. By this amendment, claims 12, 15-18 and 22-23 have been canceled, thus rendering the rejection thereof moot.

### **Claim Rejections – 35 USC § 102**

Claims 1-5, 7, 12, 19-22 and 25 were rejected under 35 USC 102(b) as being anticipated by US Patent No. 5,594,721 (hereinafter Pan) and “X.25 Packet Layer Protocol (PLP) Overview” (hereinafter PLP). With respect to claims 7, 12, 19-22 and 25, the same have canceled, thus rendering the rejection thereof moot. With respect to claim 1, applicant respectfully traverses this rejection for at least the following reasons.

The PTO provides in MPEP § 2131 that  
*“[t]o anticipate a claim, the reference must teach every element of the claim....”*

Therefore, with respect to claim 1, to sustain this rejection the *Pan and PLP* reference must contain all of the above claimed elements of the respective claim. However, contrary to the examiner’s position that all elements are disclosed in the Pan and PLP reference, the latter references do not disclose “a method of operating a non-memory mapped device memory with respect to the host processor, comprising: ... retrieving the information with a memory map controller interface, wherein responsive to the information, the memory map controller interface (i) selects the non-memory mapped device memory for a memory access if there are more than one, (ii) downloads an appropriate access protocol template of the selected non-memory mapped device memory and performs a series of transactions to setup communication with the selected non-memory mapped device memory, wherein the appropriate access protocol template

comprises a previously programmed template for each specific access type, (iii) inserts device information into an appropriate field of a message structure on an external interface bus for transfer to the non-memory mapped device memory according to the information and the template; and operating an actual data transfer with the non-memory mapped device memory according to the device information ” as recited in Claim 1. Claim 1 is believed in prima facie condition for allowance. Withdrawal of the rejection of claim 1 is respectfully requested.

Dependent claims 2-5 depend from and further limit allowable independent claim 1 and therefore are allowable as well. Withdrawal of the rejection of claims 2-5 is respectfully requested.

### **Claim Rejections – 35 USC § 103**

Claims 12 and 15-18 were rejected under 35 USC 103(a) as being unpatentable over US Patent No. 5,594,721 (hereinafter Pan) and “X.25 Packet Layer Protocol (PLP) Overview” (hereinafter PLP). With respect to claims 12 and 15-18, the same have canceled, thus rendering the rejection thereof moot.

### **Conclusion**

It is clear from all of the foregoing that claim 1 is in condition for allowance. Dependent claims 2-5 depend from and further limit independent claim 1 and therefore are allowable as well.

No amendment made was related to the statutory requirements of patentability unless expressly stated herein. No amendment made was for the purpose of narrowing the scope of any claim, unless Applicant has argued herein that such amendment was made to distinguish over a particular reference or combination of references.

In addition, the Office Action contains a number of statements characterizing the claims, the Specification, and the prior art. Regardless of whether such statements are addressed by Applicant, Applicant refuses to subscribe to any of these statements, unless expressly indicated by Applicant.

The matters identified in the Office Action of February 28, 2007 are now believed resolved. Accordingly, the application is believed to be in proper condition for allowance and an early notice of allowance is respectfully requested. Nonetheless, should any issues remain that might be subject to resolution through a telephone interview, it is requested that the Examiner telephone the undersigned at the number indicated below.

If Applicant has overlooked any additional fees, or if any overpayment has been made, the Commissioner is hereby authorized to credit or debit Deposit Account 503079, Freescale Semiconductor, Inc.

An early formal notice of allowance of claims 1-5 is requested.

Respectfully submitted,

SEND CORRESPONDENCE TO:

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